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		STUDY MODULE D	ESCRIPTION FORM	l			
Name of the module/subject  Control Engineering and computing science in industry and				Co <b>10</b>	de 10325341010324814		
Field of	study		Profile of study (general academic, practic	cal)	Year /Semester		
Elec	trical Engineerin	ng	(brak)		2/4		
Elective	path/specialty	ad Camputar Systama in	Subject offered in:  Polish		Course (compulsory, elective)		
Cycle o		nd Computer Systems in	Form of study (full-time,part-tim	10)	obligatory		
Cycle 0	,	ycle studies	part-time				
No. of h		•	•		No. of credits		
Lectu		s: - Laboratory: -	Project/seminars:	_	1		
	Classo	program (Basic, major, other)	(university-wide, from another	er field)	)		
		(brak)		(br	ak)		
Educati	on areas and fields of sci	ience and art			ECTS distribution (number and %)		
techr	nical sciences				1 100%		
Technical sciences					1 100%		
Dr inż. Jerzy Frąckowiak email: jerzy.frackowiak@put.poznan.pl tel. 616652382 Elektryczny ul. Piotrowo 3A, 60-965 Poznań							
Prere	equisites in term	ns of knowledge, skills an	d social competencie	s:			
1	Knowledge	Basic knowledge of automation,	control theory, PLCs and microcontrollers.				
2	Skills	The ability to understand and interpret the messages conveyed and effective self.					
3	Social competencies	Awareness of the need to broaden their competence.					
Assu	mptions and ob	jectives of the course:					
Knowle	edge of PLC cooperat	ion with microcontrollers.					
	Study outco	mes and reference to the	educational results for	or a	field of study		
Knov	vledge:						
	= -	ration with microcontrollers - [K_W	08++]				
		d microcontroller - [K_W08++]					
Skills		ge to work PI Co and microscotrol	lore - [K     145   1				
use the acquired knowledge to work PLCs and microcontrollers - [K_U15++]     capacity for independent thinking and creative action - [K_U15++]							
Social competencies:							
·							
Assessment methods of study outcomes							
Lectur	e:						

**Course description** 

PLCs - serial port, free port transmission mode, the selected interrupt PLC and microcontroller.

# Faculty of Electrical Engineering

## Basic bibliography:

- 1. Kamiński K.: "Programowanie w Step 7 Microwin", GRYF, Warszawa 2006.
- 2. Dokumentacja sterownika S7-1200 firmy Siemens.

#### Additional bibliography:

1. Bubnicki Z.: "Teoria i algorytmy sterowania", Wydawnictwo Naukowe PWN, Warszawa 2002.

## Result of average student's workload

Activity	Time (working hours)
1. participation in lectures	8
2. consultations for lectures	6
3. preparation for the completion of lectures	10
4. credit lecture	1

### Student's workload

Source of workload	hours	ECTS
Total workload	25	1
Contact hours	15	1
Practical activities	0	0